

SERVICE BULLETIN

AMATEUR RADIO

SUBJECT

TS-940S LCD CLOCK DISPLAY ERRATIC OPERATION

DATE

9-14-85

We have received a few reports concerning erratic operation of the LCD clock display when the Main Dial is rotated, or the VBT or SLOPE TUNE controls are operated. This appears as if you had pressed the SET, SCROLL, OR GRAPH switches.

This erratic operation may be caused by improper timing of the input pulses applied to the auxiliary display microprocessor, IC 1.

1. This symptom is easily corrected by replacing IC 2 MBM2764-30T1 with MBM2764-30T2, on Digital Unit A (X54-1830-00). Early models of the improved IC will carry the old nomenclature but will be distinguished by a white dot on the top of the chip.
2. After the IC has been changed reset the main microprocessor by pressing and holding the [A=B] key, and then turning the power switch OFF and then back ON. Then release the [A=B] key.
3. After this change is made the subdisplay will not immediately track the main display. A slight delay has been introduced in the new IC to prevent any timing errors. It may take approximately 0.5 seconds for the LCD to track the main display.
4. Please return the old IC 2 to the Service Department along with the the WSR for credit.

Perform this procedure only if the symptom occurs, as a repair. This change is not required otherwise.

Time required for this modification is 1 hour or less. ©TKC985CLM